EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"6594730".pn.	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/08/10 16:06
L2	20	(DSP same (buffer and input and output)).clm.	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/08/10 16:08
L4	148	711/135.cor. ((DSP and (coprocessor co-processor)) same (buffer and data and cache))	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/08/10 16:14
S1	14500638	@ad < "20030213"	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/02/14 14:25
S2	1	"10/774775"	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/02/14 14:46
S3	22653	S1 and "711"/\$.ccls.	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/02/14 14:46
S4	12	S3 and ((DSP with coprocessor co-processor) same (cache and (segment region)))	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/02/14 15:54
S5	215	S3 and ((DSP (coprocessor co-processor)) same (buffer and data and cache))	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/08/10 16:13
S6	114	S5 and ((input and output) same buffer)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/02/14 15:55
S7	81	S3 and ((DSP (coprocessor co-processor)) same ((buffer FIFO) with (data near20 transfer\$9)))	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/02/14 17:15
S8	106	S3 and ((DSP (coprocessor co-processor)) same ((share\$8 with memory) and (buffer fifo) and data))	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/02/14 17:17

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		<u> </u>	Γ			,
S9	27	S8 and (prefetch with data)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/02/14 17:17
S10	2	"6490649".pn.	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/02/15 07:18
S12	2	"4942553".pn.	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/02/15 08:19
S13	. 2	"5495552".pn.	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/02/15 09:54
S14	. 2	"6622208".pn.	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/02/15 10:01
S15	6	("6601126" "5012402" "5535417"). pn.	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/02/15 10:01
S16	14	(US-20020099910-\$).did. or (US-5155832-\$ or US-5742839-\$ or US-6002881-\$ or US-6101583-\$ or US-6256724-\$ or US-6490649-\$ or US-6748497-\$ or US-6801988-\$ or US-6842844-\$ or US-6925641-\$ or US-6963962-\$ or US-4942553-\$ or US-6601126-\$).did.	US-PGPUB; USPAT	OR	ON	2006/02/15 14:05
S17	7	.((DSP near10 core) same ((sequent\$9 stream\$6) with buffer))	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/08/10 08:43
S18	4131	(DSP same ((sequen\$9 stream\$6) with (buffer data)))	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/08/10 08:45
S19	136	S18 and ((prefetch\$9 pre-fetch\$9 pre-read\$8) same (memory cache))	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/08/10 10:10
S20	184	S18 and (shar\$9 with buffer)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON .	2006/08/10 10:08

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S21	681	S18 and (DSP same ((input output) with (queue buffer)))	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/08/10 10:09
S22	23	S21 and ((prefetch\$9 pre-fetch\$9 pre-read\$8) same (memory cache))	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2006/08/10 10:15



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Very Large Scale Integration (VLSI) Systems, IEEE Transactions on

Volume 6, Issue 2, June 1998 Page(s):292 - 298

Digital Object Identifier 10.1109/92.678890

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 A microprocessor with a 128-bit CPU, ten floating-point MAC's, four floati dividers, and an MPEG-2 decoder

Suzuoki, M.; Kutaragi, K.; Hiroi, T.; Magoshi, H.; Okamoto, S.; Oka, M.; Ohba, Y.; Furuhashi, M.; Tanaka, M.; Yutaka, T.; Okada, T.; Nagamatsu, M.; Urakaw: Kunimatsu, A.; Goto, H.; Hashimoto, K.; Ide, N.; Murakami, H.; Ohtaguro, Y.; A Solid-State Circuits, IEEE Journal of

Volume 34, Issue 11, Nov. 1999 Page(s):1608 - 1618

Digital Object Identifier 10.1109/4.799870

AbstractPlus | References | Full Text: PDF(640 KB) | IEEE JNL

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3. A DVD processor with dual CPUs and integrated digital front-end for advabased consumer appliances

Rygh, M.; Fratus, J.; Lee, K.; Husaini, S.; Premkumar, V.; Konstantinides, K.;

Acoustics, Speech, and Signal Processing, 2003. Proceedings. (ICASSP '03).

International Conference on

Volume 2, 6-10 April 2003 Page(s):II - 317-20 vol.2

Digital Object Identifier 10.1109/ICASSP.2003.1202358

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